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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
08/841,644	04/30/1997	SHUNPEI YAMAZAKI	0756-1603	3399
7590	06/01/2005			EXAMINER SCHILLINGER, LAURA M
Eric J. Robinson Robinson Intellectual Property Law Office 21010 Southbank Street PMB 955 Potomac Falls, VA 20165			ART UNIT 2813	PAPER NUMBER
DATE MAILED: 06/01/2005				

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	08/841,644	YAMAZAKI ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
	Laura M. Schillinger	2813	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM  
 THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

1) Responsive to communication(s) filed on 28 February 2005.  
 2a) This action is **FINAL**.                    2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

4) Claim(s) 23-32 is/are pending in the application.  
 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
 5) Claim(s) \_\_\_\_\_ is/are allowed.  
 6) Claim(s) 23-32 is/are rejected.  
 7) Claim(s) \_\_\_\_\_ is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

1) Notice of References Cited (PTO-892)  
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  
 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
 Paper No(s)/Mail Date 8/1/00, 11/13/00, 5/17/02

4) Interview Summary (PTO-413)  
 Paper No(s)/Mail Date. \_\_\_\_\_.  
 5) Notice of Informal Patent Application (PTO-152)  
 6) Other: \_\_\_\_\_

## DETAILED ACTION

### *Claim Rejections - 35 USC § 112*

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claims 23-32 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

Claim 23 recites:

A method of manufacturing an active matrix panel in which data signals are supplied to a liquid crystal layers through a plurality of thin film transistors arranged in a matrix of pixels, gate lines and data lines being coupled to each thin film transistor, said method comprising:  
forming a semiconductor layer on a substrate;

Forming a gate insulating film on the semiconductor layer;

Forming a gate electrode above the gate insulating film and gate line in electrical contact with the gate electrode;

Forming a source region and a drain region in the semiconductor layer by adding impurities thereto as donors or acceptors using the gate electrode as a self alignment mask;

Simultaneously forming an overlying gate insulator on a top and sidewalls of the gate electrode and the gate line by anodic oxidation of the gate electrode and the gate line to reduce the dimensions of the gate electrode and the gate line and simultaneously form a lateral offset, AL, from the source region and the drain region to the sidewalls of the gate electrode; and

Forming a data line in electrical contact with the source region and crossing over the gate line at a cross-over location, wherein the overlying gate insulator is located between the data line and the gate line at the cross-over location to insulate the data line from the gate line.

Applicant's specification fails to disclose and therefore does not enable a simultaneous anodic oxidation of a gate electrode and a gate line, Applicant's specification refers to a common gate wire and a gate wiring- however does not teach to implement anodic oxidation on either structure, let alone do so simultaneously with the gate electrode. Further, Applicant's disclosure fails to enable a data line let alone a data line in electrical contact with a source region which crosses over a gate line at a cross-over location. Applicant's specification fails to disclose a "cross over location".

Applicant's specification fails to disclose the formation of a gate insulator between a data line and a gate line at the cross over location.

It is not possible for one of ordinary skill in the art to make Applicant's invention described in his claims based upon his disclosure because there is a total absence of teachings with respect to a data line, a gate line, and a cross over location.

Consequently, Applicant's claims are rejected.

#### ***Response to Arguments***

Applicant's arguments filed 2/28/05 have been fully considered but they are not persuasive. Applicant argues that the chart provided with the remarks indicates the support for applicant's claim language. However the chart refers to application 08/504225; not the current application 08/841644. Since Applicant's arguments are based on a different patent application

they are not persuasive to overcome the Examiner's 112 rejection. Consequently, the rejection is made FINAL.

*Conclusion*

**THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Laura M. Schillinger whose telephone number is (571) 272-1697. The examiner can normally be reached on M-T, R-F 7:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl W. Whitehead, Jr. can be reached on (571) 272-1702. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Laura M Schillinger  
Primary Examiner  
Art Unit 2813

5/16/05